MOSart Labs

Art of VLSI

Advanced Design Verification Program

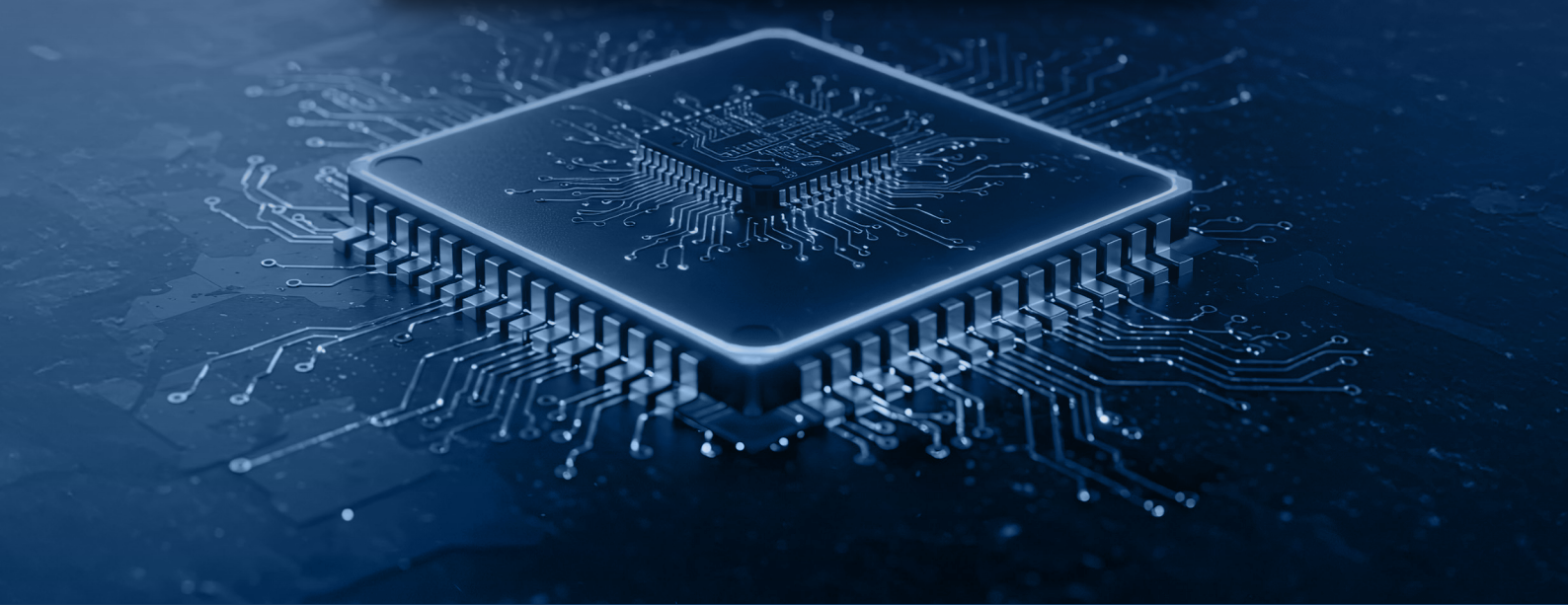
(Offline Mode at our Hyderabad Centre)

Your Gateway to Semiconductor World

- 6 Months Industry Ready Program
- 100% Placement-Oriented Program
- Full-time offline course
- Real chip design projects
- 2 Months Internship included (IP/Industry project)
- Hands-on training with industry-standard tools

Get trained by **India's leading chip design experts**

ams OSRAM · Micron · intel



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About Design Verification Program

The semiconductor industry is rapidly expanding, creating strong demand for skilled Verification Engineers. MOSart Labs is an active chip design company, not just a training institute, offering the Advanced Design Verification Offline Program to build industry-ready verification professionals.

Learn inside a real chip design environment with hands-on training in SystemVerilog, UVM, Assertions, Functional Coverage, Verification IP, Debugging, Regression, and industry-standard EDA tools while working on practical verification projects.

The 6-Month Offline Program, including a 2-Month Industry Internship Project, combines expert mentorship, real project experience, and industry exposure to prepare you for a successful career in Design Verification.

Learn Inside a Chip Design Company

Train inside an active chip design company with real industry workflows.

Full-Time Classroom Learning

Offline instructor-led training with personalized mentoring and continuous practical lab sessions.

500+ Hours of Verification Labs

Gain extensive hands-on coding, debugging, coverage, and verification experience.

2-Month Industry Internship

Work on real verification projects with expert guidance.

Learn from Industry Veterans

Learn directly from experienced VLSI professionals with decades of industry expertise.

High-Paying Career Opportunities

Build job-ready Physical Design skills for leading semiconductor companies.

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Inside MOSart Labs







Offline Classes | 9:30 AM to 5:30 PM











100+ Potential Hiring Companies

Texas Instruments · ams OSRAM · Micron · NVIDIA · cadence · SYNOPSYS® · QUALCOMM®

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2 Month Industry Project Internship

At MOSart Labs, an active chip design company, learning extends beyond the classroom. During the 2-month internship, you'll execute a complete Design Verification project following professional semiconductor verification methodologies.

From Project Setup & Verification Planning to Testbench Development, UVM Environment Development, Functional Verification & Debugging, Verification Sign-off, and Industry Project Presentation, you'll experience the complete verification lifecycle under the guidance of experienced VLSI engineers.

Build practical project experience, strengthen your portfolio, and graduate with the confidence to contribute to real semiconductor verification teams from day one.

Internship Project

A 2-month hands-on verification project where students develop UVM-based verification environments, execute regressions, achieve coverage closure, and validate complex digital designs.

1
Project Setup & Verification Planning
Analyze design specifications and prepare a comprehensive verification plan and strategy.

2
Testbench Development
Develop SystemVerilog testbenches with drivers, monitors, scoreboards, and assertions.

3
UVM Environment Development
Build a reusable UVM verification environment with agents, sequences, and test scenarios.

4
Functional Verification & Debugging
Execute regressions, debug design issues, and achieve functional coverage goals.

5
Verification Sign-off
Perform coverage closure, regression analysis, & complete verification sign-off activities.

6
Industry Project Presentation
Present the complete verification project with reports, documentation, and technical review.

+91-7207224587 https://mosartlabs.com/ admissions@mosartlabs.com

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A 6 Months Journey - Advanced Design Verification

Phase 1 (Offline)
2 Months (DV Foundation) with Course Project

- Learn verification fundamentals.
- Master System Verilog, SVA, coverage, and debugging.
- Build a strong verification foundation.

Phase 2 (Offline)
2 Months (Advanced DV) with Course Project

- Learn UVM, VIP, protocols, and regression.
- Apply advanced verification concepts through hands-on projects.

Phase 3 (Offline)
2 Months (Industry Internship Project)

- Get your internship project reviewed by industry experts.
- Work on an industry-level Design Verification project.
- Gain real semiconductor industry experience.

Phase 4
Internship Project Review

- Extended internship project with hands-on experience on real-world design challenges.
- Weekly 1:1 mentorship sessions with design experts for personalized guidance and feedback.

Phase 5
Job Interview Preparation

- Build your portfolio and attend mock interviews.
- Prepare thoroughly for placement opportunities.

Phase 6
Get Placed

- Attend interviews scheduled by our placement team.
- Crack your interviews and enter the semiconductor industry.

OUR LEADERSHIP TEAM



Rajesh Gupta
27 Years Experience
Ex- Country Head
Micron & ams OSRAM



Dr. Krishna Kanth
23 Years Experience
Ex-Director
ams OSRAM

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What You Will Learn

Foundation Module

Design Verification

- System Verilog Fundamentals
- Testbench Development
- Constrained Random Verification
- Functional Coverage
- Code Coverage
- Assertions (SVA)
- Scoreboards & Drivers
- Debug & Verification Flow
- Industry EDA Tools
- Verification Project

Specialization Module

Advanced Design Verification

- UVM Methodology
- Advanced Verification Techniques
- Verification IP (VIP)
- Protocol Verification
- Coverage Closure
- Assertion-Based Verification
- Analog/Mixed-Signal Verification
- SoC/IP Verification
- Regression & Debugging
- Industry Verification Project

Career Opportunities

- Design Verification Engineer
- UVM Verification Engineer
- Functional Verification Engineer
- IP Verification Engineer
- Verification Validation Engineer
- SoC Verification Engineer

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Program Outcomes

By the end of this program, you'll have the technical skills, practical experience, and confidence to begin your career in Design Verification.

Become an Industry-Ready Verification Engineer
Master modern verification methodologies with hands-on project experience.

Execute the Complete Verification Flow
Gain expertise from verification planning to UVM, regression, coverage closure, and sign-off.

Build a Strong Industry Portfolio
Showcase industry-level verification projects completed during coursework and internship.

Industry Internship Experience
Gain real-world exposure by verifying industry-grade digital designs.

Master Industry-Standard EDA Tools
Gain practical experience with leading verification and simulation tools.

SYNOPSYS® cadence®